# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,723,656 B2 Page 1 of 8

APPLICATION NO. : 09/902931
DATED : April 20, 2004
INVENTOR(S) : Kirk Martin

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

The title page should be deleted to appear as per attached title page. The sheets of drawings consisting of figures 1-7 should be deleted to appear as per attached figures 1-7.

Signed and Sealed this

Eighteenth Day of September, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office

# (12) United States Patent Martin

(10) Patent No.:

US 6,723,656 B2

(45) Date of Patent:

Apr. 20, 2004

# (54) METHOD AND APPARATUS FOR ETCHING A SEMICONDUCTOR DIE

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Watsonville, CA (US)

(\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35

U.S.C. 154(b) by 27 days.

(21) Appl. No.: 09/902,931

(22) Filed: Jul. 10, 2001

(65) Prior Publication Data US 2003/0013317 A1 Jan. 15, 2003

### (56) References Cited

#### U.S. PATENT DOCUMENTS

4,359,360 A	٠	11/1982	Harris et al 156/345.18
4,372,803 A	•	2/1983	Olganic 156/345
			Miller 438/16
5,252,179 A	٠	10/1993	Ellerson et al 216/90
5,955,142 A	٠	9/1999	Muller et al 356/357
6,238,936 B1	٠	5/2001	Yu 438/7

#### OTHER PUBLICATIONS

Weavers, Barry A., "Chemical Thinning of Silicon for Emission Microscopy Using Multi ETGH—An Introduction," Nisene Technology Group, Inc., B&G International Division, Aug. 2000, pp. 1-6.

Hypervision, "Hypervision's PTF1 Portable Test Floor

Mypervision, "Hypervision's P1F1 Pottable 1est Floor Emission Microscope," Hypervision Inc., 2000, pp. 1–8. Hypervision, "Sensor and Optics Technology: BEAMS and Morcad Telluride (MCT)", Hypervision Inc., 2000, pp. 1–8. Hypervision, "Chip UnZip Backside Preparation Tool," Hypervision Inc., 2000, pp. 1–4.

Hypervision, "Chip UnZip: Low Stress Backside Semiconductor Preparation," Hypervision, 1998, pp. 1-4.

Adams, Tom, "Backside Emission Microscopy of Wafers," News and Analysis @Semiconductor Online, Feb. 28, 2000, pp. 1-3.

Adams, Tom, "Bringing the Emission Microscope to the Test Floor," News and Analysis @Semiconductor Online, Jul. 1, 1999, pp. 1-4.

Hypervision, "BEAMS'M (Backside Emission Analysis Microscope System)", Hypervision Inc., 1998, p. 1.

Clark, Scott, MSCE, "Etching Silicon Dioxide with Aqueous HF Schttions," Silicon Dioxide Etch, Bold Technologies, Jan. 29, 2001, pp. 1-5.

Bold Technologies, "Manual Equipment", Bold Technologies, Inc., 1998-2000, pp. 1-3.

Nisene Technology Group, "MultiEtch System," Nisene Technology Group, Inc., Jul. 12, 2000, pp. 1-4.

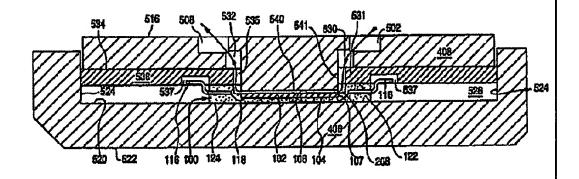
#### " cited by examiner

Primary Examiner—Lan Vinh
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Carmen C. Cook

## (57) ABSTRACT

A method and apparatus for etching a semiconductor die are disclosed whereby flowing an etchant material across an inactive thereof thins the semiconductor die. In one embodiment, the atchant includes a mixture of nitric acid, bydrofluoric acid, and acetic acid and turbulently flows from one edge of the semiconductor die, across the inactive surface of the semiconductor die, to an opposing edge of the semiconductor die.

### 26 Claims, 7 Drawing Sheets



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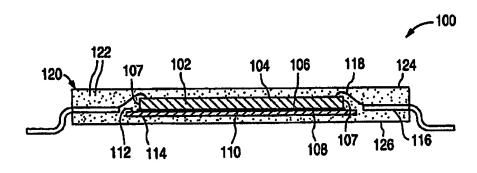


FIG. 1 (Prior Art)

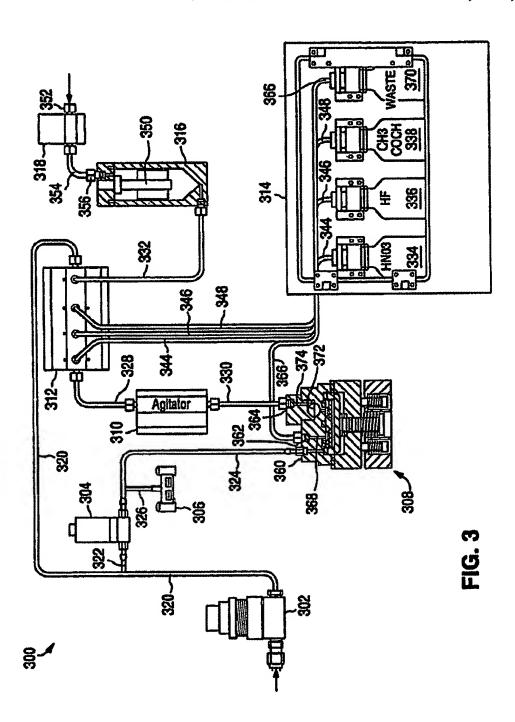
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FIG. 2

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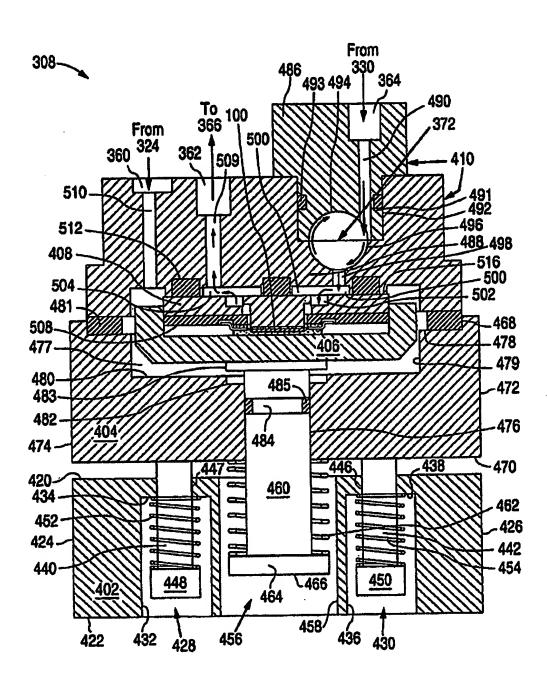
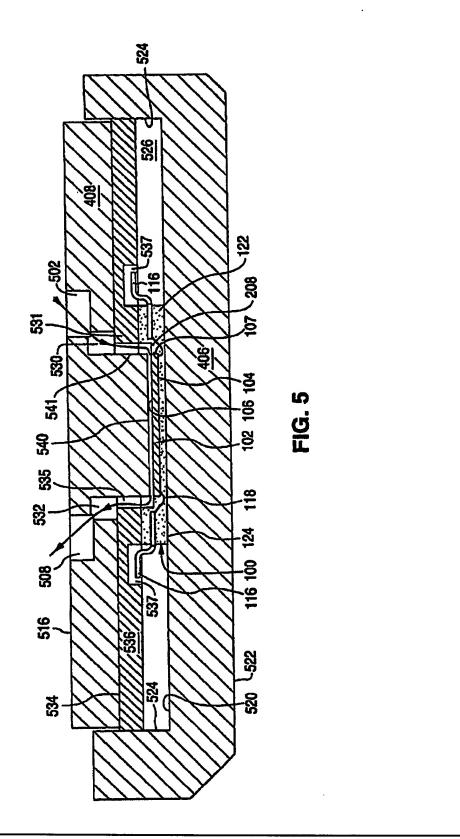
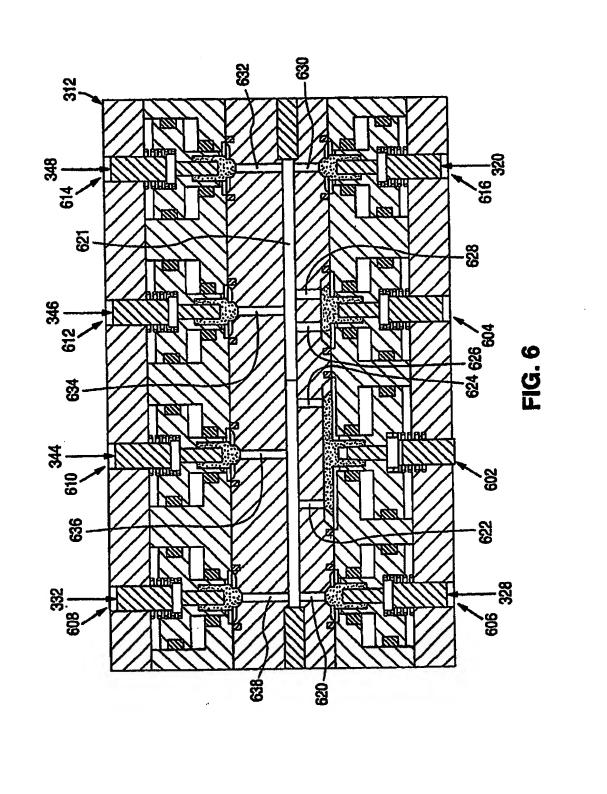


FIG. 4

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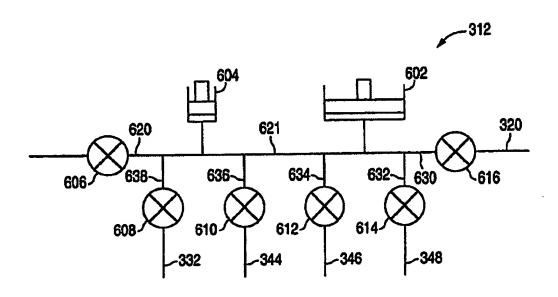
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**FIG. 7**